



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors: Deva N. Pattanayak,  
Assignee: Siliconix incorporated  
Title: Triple-Diffused Trench MOSFET And Method Of Fabricating The Same  
Serial No.: 10/657,830 Filing Date: 09/08/03  
Examiner: Mai-Huong Tran Group Art Unit: 2818  
Docket No.: SCX015 US Confirmation No.: 1749

Santa Clara, California  
December 21, 2004

COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO OFFICE ACTION**

Dear Sir:

In Response to the Office Action dated November 9, 2004, Applicants elect without traverse to have the invention of Group II, Claims 1-11, examined. Please withdraw from consideration Claims 12-18, which are drawn to the invention of Group I. Applicants reserve the right to seek examination of the invention of Group I in a divisional application.

Should the Examiner have any questions concerning this response, the Examiner is invited to call the undersigned at (408) 982-8200, ext. 1.

**CERTIFICATE OF MAILING BY "FIRST CLASS"**

I hereby certify that this paper or fee is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the below date.

*David E. Steuber*  
Attorney for Applicant(s)

*12/21/04*  
Date of Signature

Respectfully submitted,

*David E. Steuber*  
David E. Steuber  
Attorney for Applicant(s)  
Reg. No. 27,557

SILICON VALLEY  
PATENT GROUP LLP  
2350 Mission College Blvd.  
Suite 360  
Santa Clara, CA 95054  
(408) 982-8200  
FAX (408) 982-8210